

10/517258

DT09 Rec'd PCT/PTO 07 DEC 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :  
Katsuhiro NAKAI et al. : **Mail Stop: PCT**  
Serial No. NEW : Attorney Docket No. 2004\_1946A  
Filed December 7, 2004 :  
SEMICONDUCTOR INTEGRATED CIRCUIT:  
DEVICE, DATA STORAGE VERIFICATION  
DEVICE, AND DATA STORAGE :  
VERIFICATION METHOD  
[Corresponding to PCT/JP03/07541  
Filed June 13, 2003]

PRELIMINARY AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

THE COMMISSIONER IS AUTHORIZED  
TO CHARGE ANY DEFICIENCY IN THE  
FEES FOR THIS PAPER TO DEPOSIT  
ACCOUNT NO. 23-0975

Sir:

Prior to initial examination of the above-identified application, please amend the application  
as follows:

ATTACHMENT E